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Exercise Solutions Exercise 1.77 Exercise 1.79 No, there is no legal set of logic levels. The slope of the transfer characteristic never is better than -1, so the system never has any gain to compensate for noise. Exercise 1.81 The circuit functions as a buffer with logic levels $V_{IL} = 1.5$; $V_{IH} = 1.8$; $V_{OL} = 1.2$; $V_{OH} = 3.0$. It can receive inputs from LVCMOS and LVTTL gates be-

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Chapter 3 Computer Exercise: 13. You may use the MATLAB code provided in the course for this exercise. Note: change the sample 4 for π 1 from AD to ADB. Chapter 5: 5, 10. Chapter 5 Computer Exercise: 2. Chapter 6: 6. EM problem: Second-order methods in three-layer networks, introduction to stochastic methods: Thursday: 5/8. 4:15-5:30pm

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